

Claims

We claim:

- 5 1. A buffer circuitry for buffering a radio-frequency (RF) signal, comprising:
a complementary pair of switches having an input terminal and output terminal, the input
terminal of the complementary pair of switches configured to respond to the
radio-frequency signal, the output terminal of the complementary pair of switches
coupled to an output of the buffer circuitry; and
10 a power source, including a capacitor coupled to a current source, the power source
coupled to the complementary pair of switches, the power source configured to
supply power to the complementary pair of switches such that the buffer circuitry
supplies a substantially constant power level at its output.
- 15 2. A radio-frequency (RF) apparatus, comprising:
a first circuit partition, comprising receiver analog circuitry configured to produce a
digital receive signal from an analog radio-frequency signal; and
a second circuit partition, comprising receiver digital circuitry configured to accept the
digital receive signal, wherein the first and second circuit partitions are partitioned
20 so that interference effects between the first circuit partition and the second circuit
partition tend to be reduced.